

On the effect of water-induced degradation of thin-film piezoelectric microelectromechanical systems

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Abstract - Lifetime and reliability in realistic operating conditions are important parameters for the application of thin-film piezoelectric microelectromechanical systems (piezoMEMS) based on lead zirconate titanate (PZT). Humidity can induce time-dependent dielectric breakdown at a higher rate compared to dry conditions, and significantly alter the dynamic behavior of piezoMEMS-devices. Here we assess the lifetime and reliability of PZT-based micromirrors with and without humidity barriers operated at 23 °C in an ambient of 0 and 95 % relative humidity. The correlation of the dynamic response, as well as the ferroelectric, dielectric, and leakage properties, with degradation time was investigated. In humid conditions, the median time-to-failure was increased from 2.7×10^4 [$1.9 \times 10^4 - 4.0 \times 10^4$] s to 1.1×10^6 [$0.9 \times 10^6 - 1.5 \times 10^6$] s at 20 V_{AC} continuous unipolar actuation, by using a 40 nm thick Al₂O₃ humidity barrier. However, the initial maximum angular deflection, polarization, and dielectric permittivity decreased by about 6, 11, and 12 %, respectively, for Al₂O₃ capped devices. For both bare and encapsulated devices, the onset of electrothermal breakdown-events was the dominant cause of degradation. Severe distortions in the device's dynamic behavior, together with failure from loss of angular deflection, preceded time-dependent dielectric breakdown in 95 % relative humidity. Moreover, due to the film-substrate stress transfer sensitivity of thin-film devices, water-induced degradation affects the reliability of thin-film piezoMEMS differently than bulk piezoMEMS.

Index Terms – piezoelectricity, microelectromechanical systems (MEMS), thin-films, lead zirconate titanate, humidity barrier, humidity-induced degradation, reliability, lifetime, atomic layer deposition, breakdown

I. INTRODUCTION

LEAD zirconate titanate (PZT) is a high-performance piezoelectric material widely used for microelectromechanical systems (MEMS)[1], [2].

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Piezoelectric thin-films have promising properties for MEMS applications [3], making them attractive for adjustable optics[4]–[6], transducers[7]–[9], FERAM[10]–[12] and energy harvesters[13]–[16]. Ensuring a reliable long-term device operation in realistic and harsh operating conditions is technologically and scientifically important for commercialization of thin-film piezoelectric MEMS (piezoMEMS)[17]–[20]. As such, humidity is a severe ambient stressor[21]–[24]. Operation in humid conditions causes cracking, resistance degradation, excessive leakage, and time-dependent dielectric breakdown (TDDB) considerably faster than for operation under dry conditions[25], [26]. For PZT-based piezoMEMS, early onset of transient leakage currents and excessive Joule-heating, which locally vaporize the stack material, has been well established as one of the main degradation effects[27]–[29]. For devices, such electrothermal breakdown-events (ETB) are irreversible (hard) degradation, and may, in addition to TDDB, cause loss of active electrode area, film delamination, decreased structural integrity of the piezoelectric layer-stack and degraded properties of the piezoelectric[30]. Such degradation are expected to affect the dynamic behavior of devices throughout their lifetime and need to be considered, and particularly important to understand, monitor and mitigate for thin-film piezoMEMS, since their functionality is sensitive to the film-substrate stress transfer.

Applying an adequate humidity barrier is generally imperative for improving the reliability and lifetime of piezoMEMS devices[31]–[33]. However, encapsulating thin-film piezoMEMS-devices is challenging for several reasons: (i); devices contain electromechanically moving parts and require mechanically robust and durable humidity barriers, (ii); covering large complex, structured surfaces with a uniform, pinhole-free and sufficiently thin coating, can be challenging, (iii); the additional encapsulation step may affect the properties of the final device, and (iv); the encapsulation method must be CMOS-compatible with the possibility for wafer-level integration[34], [35]. Several encapsulation techniques have been tested, for example using polymers such as parylene-c[36] or epoxy[37]. For thin-film piezoMEMS-devices, Al₂O₃ is a promising barrier material that can be applied using CMOS-compatible techniques due to its low water vapor transmission

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rate, high dielectric strength, and good mechanical strength[32], [38]. Atomic layer deposition (ALD) is a promising deposition technique for applying thin Al_2O_3 humidity barriers as it complies well with point (i)-(iv) and uses low-cost precursors[39].

The lifetime of piezoMEMS-devices is often assessed by direct current (DC) TDDB measurements accelerated by electric fields and elevated temperatures[18], [25], [40]–[43]. DC electrical field reliability relates to the various charge-trapping processes and conduction-mechanisms of the thin-film. The leakage current behavior depends on the doping-type and concentration, the films' defect chemistry, thickness, microstructure as well as the characteristics of the PZT-electrode interface[43]–[49]. However, although DC TDDB measurements are important for analyzing essential processes related to the device lifetime, such tests do not provide insight into the device's dynamic response during degradation, and additional possible failure modes, specific for a unique device geometry[33], [50]. It is, therefore, of interest to study how degradation affects the dynamic behavior as well as the dielectric properties of factual devices during operation. Furthermore, the rate of humidity-related degradation depends on the relative humidity at the surface of the device, and may thus be shadowed by elevated temperatures, often used for accelerating lifetime tests.

This study assesses the electromechanical, piezoelectric, and dielectric/ferroelectric properties of thin-film PZT-based micromirrors during operation at room temperature in dry and humid conditions. The intention is to investigate the effect of humidity-related degradation, and the use of ALD-deposited Al_2O_3 as a strategy for mitigating such degradation, from the perspective of a factual piezoMEMS device. Micromirrors developed at SINTEF MiNaLab[4], were chosen as a model system for this purpose. Here, the development and use of a compact, retrofittable experimental setup allowed for characterizing essential electrical and dynamic aspects related to the reliability of up to five devices simultaneously under controlled humid operating conditions. A two orders of magnitude improved lifetime using humidity barriers is demonstrated. The origins of device failure in humid conditions and important effects of humidity-related degradation on the dynamic behavior of the micromirrors prior to failure are highlighted and discussed.

II. EXPERIMENTAL

Thin-film piezoelectric micromirrors with a functional layer-stack of $\text{Au}/\text{TiW}/\text{PZT}/\text{LaNiO}_3/\text{Pt}/\text{Ti}/\text{SiO}_2/\text{Si}$ were fabricated using $1.01 \pm 0.02 \mu\text{m}$ thick Ba-doped $\text{PbZr}_{0.4}\text{Ti}_{0.6}\text{O}_3$ (PZT) films with a 90 % / 10 % mixed (001) / (110)-orientation. The PZT thin-films were deposited onto a 10 nm (001)-oriented LaNiO_3 buffer/seed-layer using pulsed laser deposition (Solmates SIP 800) on a 6" platinized silicon-on-insulator wafer[34], [51]. The top electrodes, comprised of 250 nm Au on top of a 10 nm Ti with 10 wt% W adhesion-layer, deposited by DC magnetron sputtering, before device definition. A bake out step of 15 minutes at 200 °C was done before electrode deposition to remove potential water-vapor from the substrate. For the present layer stack, the PZT films had a breakdown strength of 500-550 kV/cm with positive and 450-500 kV/cm with negative

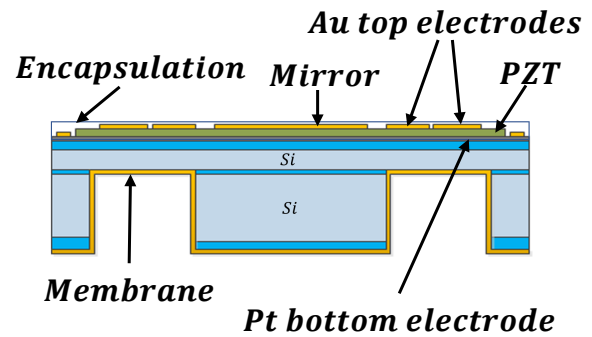


Fig. 1: Side-view schematic of the micromirror. Extended information on device design and functionality can be found in previous work[4], [27].

bias on the top electrode. Positive top electrode bias was, therefore, used in this study. An average stress coefficient of $e_{31,f} = -12.5 \pm 0.3 \frac{\text{C}}{\text{m}^2}$ was measured by a strain-gauge method [52]. In previous studies [4], it was shown that poling of this particular device geometry resulted in a decreased angular deflection, thus no poling of the devices were done before the experiments. The Al_2O_3 encapsulation was deposited as the last step, using a Beneq TFS-500 ALD reactor maintained at 175 °C, with a working pressure of 3 mbar. The encapsulation had a thickness of 37-40 nm, measured using a J. A. Woolam a-SE spectroscopic ellipsometer. To enable wire-bonding, buffer HF was used to etch through the encapsulation in the middle of the contact pads. The Al_2O_3 -layer is expected to add an additional stress of about 400 MPa to the layer-stack [38], [53].

Qualification tests of the pristine devices showed that the PZT films endured more than 1×10^{11} degradation-free unipolar fatigue-cycles at 100 kHz and the maximum intended operating voltage of 20 V_{AC} around a 10 V_{DC} offset (0-200 kV/cm) in ambient air of 35 % relative humidity (RH) and 23 °C. At 20 V the devices experience a maximum in-plane strain of 0.093 %. The reader is otherwise directed to the supplementary information for more details.

Fig. 1 shows a cross-section schematic of a final device, diced from the processed 6" wafer. The device comprised a 400 μm thick silicon-disc with a diameter of 3 mm, suspended on an 8 μm thick silicon membrane, structured with backside deep reactive ion etching. The PZT-film was continuous across the 8x8 mm² die. Using standard lithography, eight electrodes were defined to form an inner and outer ring on the membrane surrounding the center disc. These were routed by 20 μm wide electrode tracks to bonding pads placed along the outer edge of the die. For the present design, actuating the inner electrodes pushes the centre disc downwards, and actuating the outer electrodes pushes the disc upwards. Here, generating a maximum tip-tilt motion was done by actuating the two inner and two outer electrodes on each opposite sides of the device, as illustrated in Fig. 2 (a). More details on the design and fabrication of the used devices can be found elsewhere[4], [27].

The dynamic behavior was characterized by reflecting a laser off the micromirror's center disc onto a Thorlabs PDP90A position sensitive device (PSD), as shown schematically in Fig. 2 (a). Since the maximum tilt was always less than 1°, the small-angle approximation, $\tan(\theta) \approx \theta$, was used to calculate

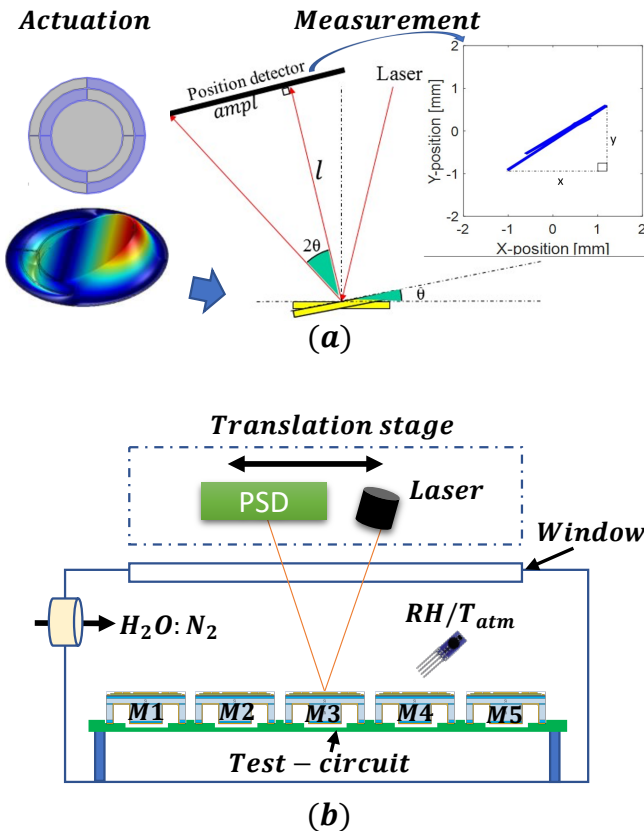


Fig. 2: A schematic of device-actuation and setup for electromechanical characterization is shown in (a). (b) Measurements were carried out in a compact environmental chamber designed for lifetime and reliability testing of piezoMEMS-devices. The ambient humidity was controlled by bubbling N_2 through a DI-water container during testing. The position detector and laser were fixed on a micrometre stage outside the chamber allowing simultaneous characterization of five piezoMEMS-devices in each condition. The setup was retrofitted to an AixACCT TF2000 for leakage, ferroelectric and dielectric characterization of each micromirror.

the amplitude of the angular deflection, θ , based on the recorded x and y laser-trace positions:

$$2\theta \approx \tan(2\theta) = \frac{\sqrt{x^2 + y^2}}{l} \quad (1)$$

This is illustrated to the right in Fig. 2 (a), where $l = 100$ mm is the distance from the device to the PSD, and x and y are the coordinates of the laser trace on the PSD. The x and y coordinates were used to study the evolution of the devices' dynamic response with degradation-time. A maximum angular deflection of 0.6° was obtained for bare devices at $20 V_{AC}$ and an excitation frequency of 1.5 kHz, far away from the resonance-frequency of 9 ± 1 kHz. This corresponded to a $15.7 \mu\text{m}$ distance from the bottom of the tilted center mirror-plane to the neutral plane, during actuation. Throughout the lifetime measurements, the devices were unipolarly cycled using $20 V_{AC}$, offset around $10 V_{DC}$ at 1.5 kHz.

The experimental setup, designed and used for this study, is shown schematically in Fig. 2 (b). For each test condition, five micromirrors were wire-bonded to a polyimide MEMS test circuit and mounted inside a custom-made compact environmental chamber. The humidity inside the chamber was controlled by bubbling N_2 through a DI-water container and flowing the $H_2O:N_2$ -mixture through the chamber during the

experiment. Each condition was stabilized prior to, and controlled throughout the measurements, through a LabView-program with a HYT271 humidity sensor. In humid conditions, the ambient was kept at 95% RH at room temperature (23°C). Dry conditions, $\sim 0\%$ RH, were achieved by flowing dry N_2 through the chamber.

All measurement equipment was kept outside the chamber and contacted to the test circuit using high-temperature wires. Characterizing the dynamic behavior of all five devices was done by mounting the laser and PSD onto an external micrometre stage, moving the laser over each device to be characterized during the experiment. This was enabled by reflecting the laser of the micromirrors through an optical viewport integrated in the environmental chamber. Each device was scanned for 10 seconds before moving to the next micromirror, and the cycle repeated throughout the experiment. Each cycle, scanning across all five micromirrors, had a total duration of 1 minute.

To enable dielectric and ferroelectric characterization, the experimental setup was retrofitted to an AixACCT TF2000 ferroelectric analyzer. The average of 5 polarization-field hysteresis loops was measured at 100 Hz with a large-signal of $20 V_{AC}$. For measurements of the permittivity and loss, a small-signal amplitude of 200 mV with a small-signal frequency of 1 kHz was used.

Two failure-conditions were defined: (1) more than $1/e$ loss of angular deflection and (2) TDDB taken as the time when the leakage current increased by two orders of magnitude. The failure probability was calculated using the cumulative distribution function[54], [55]:

$$f(t; \alpha, \beta) = 1 - e^{-\left(\frac{t}{\alpha}\right)^\beta} \quad (2)$$

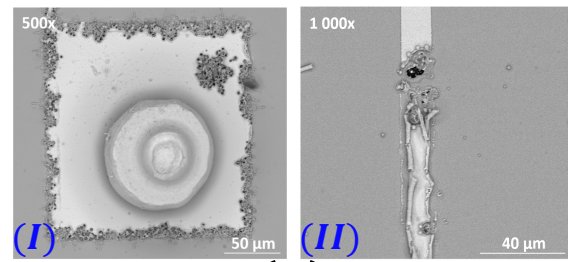
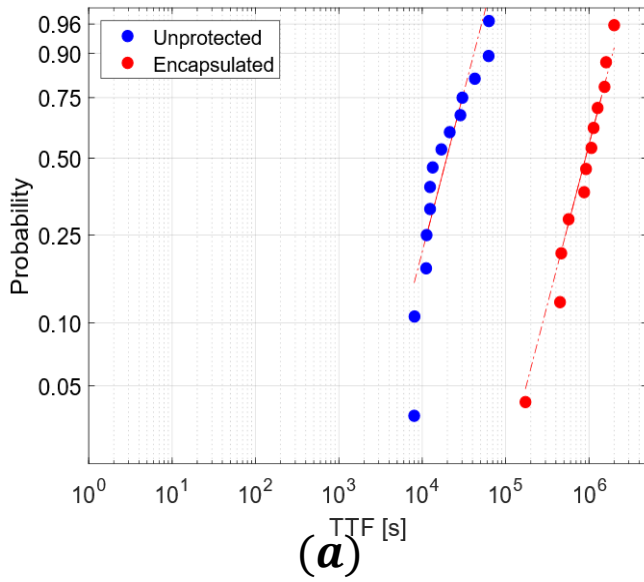
where β is the shape parameter describing the confidence of the measurement, α the reported time to failure and t the time of operation. Structural characterization was done using optical (Pixelink PL-B623) and scanning-electron microscopy (Quanta 600 FEG SEM). ImageJ image analysis software was used to analyze the post-degradation crater sizes on failed devices.

III. RESULTS AND DISCUSSION

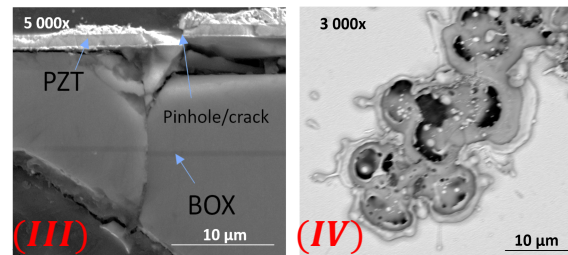
A. Device lifetime and critical flaws

Fig. 3 (a) shows the failure probability-distribution for bare (blue) and encapsulated (red) devices in humid conditions. Applying an Al_2O_3 encapsulation improved the median cycles-to-failure by two orders of magnitude from 4.1×10^7 [$2.9 - 6 \times 10^7$] to 1.7×10^9 [$1.4 - 2.3 \times 10^9$]. The confidence intervals are given in the brackets. This corresponds to an increase in MTTF from 8 hours to 314 hours with continuous cycling at maximum specified loading conditions of 200 kV/cm. the corresponding Weibull shapes of $\kappa_{bare} = 1.5$ and $\kappa_{encap} = 2.0$, indicates an increasing failure rate with time, for both bare and encapsulated devices, and will be discussed below.

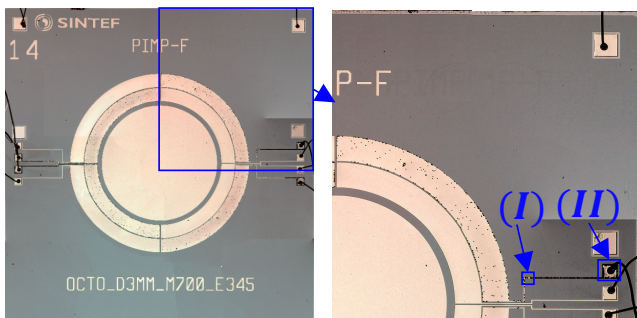
Fig. 3 shows optical images of degraded bare, (b), and encapsulated, (c), devices, after failure. As was also found in previous work[27], all bare devices tested here failed exclusively due to a breach of one of the electrode routings. The loss of electrical connection to the actuation membrane resulted



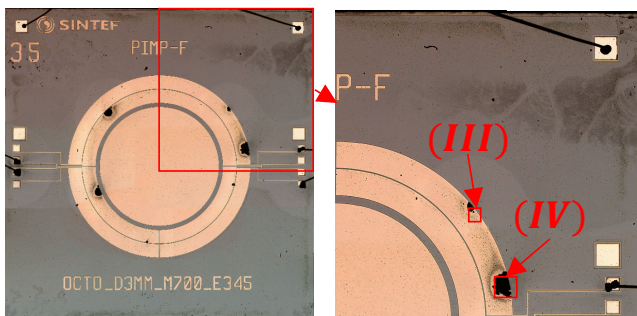
(a)



(b)



(b)



(c)

Fig. 3: Failure probability as a function of time is shown in (a), and optical images of bare and encapsulated devices, post-failure, are shown in (b) and (c). A close-up of a region of interest is shown to the right in (b) and (c) and analyzed further by SEM in Fig. 4. During degradation, ETB-events with an average post-failure crater size of $150 \pm 40 \mu\text{m}^2$ appeared more frequently along the electrode edges for bare devices than for encapsulated. In comparison, the encapsulated devices displayed similar degradation at localized points, with an average post-failure crater size of $2.7 \times 10^4 \pm 1.5 \times 10^4 \mu\text{m}^2$.

in a power cut and therefore a drop in angular deflection. This contrasts with a gradual loss of angular deflection driven by e.g., polarization or mechanical fatigue. The failure was predominantly driven by ETB-events on the active top electrode areas (anode), eventually causing a connection breach due to material melting/evaporation, and/or film delamination.

Fig. 4: Post-failure SEM-images of bare, (a) and encapsulated (b) devices from points (I)-(IV) indicated in Fig. 3. Smaller craters more frequently located along the electrode edges than on the membrane surface was found on bare devices, compared to encapsulated devices. The latter displayed, fewer, larger, and more localized failure-points, containing large cracks all the way through the device-layer as shown in (III) and (IV).

Examples of such ETB-events and routing breaches, post-failure, are shown for bare devices in the optical images of Fig. 3 (b). SEM images of the indicated points, (I) and (II), are shown in Fig. 4 (a). Post-failure, the ETB-craters of the bare devices covered an average $150 \pm 40 \mu\text{m}^2$ of the electrode surface area, with most of the craters appearing along the electrode edges. From elemental mapping of post-failed devices (not shown here), residuals of Si, PZT, Pt, and Au were found both inside the post-failure craters and scattered across the electrode surface, consistent with degradation observed in previous work[24], [27] and with other literature reports[24]. For the present device design the critical flaw was located at the $20 \mu\text{m}$ wide electrode routings, which make up about 1/3 of the total electrode circumference (approx. 11 mm). Since the width of the routings is comparable in size to the ETB craters, they are particularly prone to failure during degradation, as exemplified in (II) of Fig. 4 (a).

For the encapsulated devices, degradation was manifested as an intermixing of amplitude-loss and TDDDB. This is also reflected in $\kappa_{encap} > \kappa_{bare}$, and devices failing more rapidly with time after the initial onset, since more degradation-mechanisms come into play. As indicated by point (III) and (IV) of Fig. 3 (b) and Fig. 4 (b), the more localized and considerably more extensive ETB craters had average crater size of $26\,000 \pm 15\,000 \mu\text{m}^2$. They appeared close to the wire-bonding pads, at localized points along the electrode edges, and on top of the electrode-surface. For the latter, sputtered particles in the piezoelectric was found to correlate directly with localized ETB events (see supplementary information). This indicates that various defects, such as large PZT particles deposited during the PLD process, microcracks from the

process of wire-bonding or small nonuniformities in encapsulation coverage at step-edges etc., are vulnerable points on the encapsulated device, contributing to encapsulation breach and degradation onset. Additionally, large stress/strain-gradients, particularly at the electrode edges, may also contribute to such cracking and encapsulation-breach when the electromechanical deflection is high, here up to $15\ \mu\text{m}$ at a 20 V bias. After an encapsulation-breach, degradation in the form of ETB events and further cracking can proceed and spread from its initial site along the device for an extended period, without necessarily inducing immediate device failure. This enlarges the post-failure crater-sizes with time and induce considerable damage to the membrane structure, as well as loss of top electrode-area on encapsulated devices as exemplified in (III)-(IV) of Fig. 4 (b) (see the supplementary information for more details).

B. Leakage transients and effect on dynamic behavior

The evolution of leakage together with the dynamic behavior was recorded throughout the degradation and is exemplified in Fig. 5 (a). The blue and black curves show typically encountered leakages as function of degradation time for a bare device and for an encapsulated device, respectively. TDDB is indicated by the dotted lines and relate the leakage-measurements in Fig. 5 (a) with the corresponding deflection measurements in Fig. 5 (b). A bare device operated in dry ambient is included for reference (green curve). From the leakage vs. time measurements in humid conditions, the degradation displayed traits similar to that of typical TDDB in dry conditions, but on a much reduced timescale. Leakage transients were observed in humid atmosphere after about $6 \times 10^3\ \text{s}$ ($\sim 9 \times 10^6$ cycles) for bare and after about $8 \times 10^4\ \text{s}$ ($\sim 1 \times 10^8$ cycles) for encapsulated devices. In comparison, few or no transients, and no dielectric breakdown were recorded within the experimental timeframe in dry conditions, as shown by the green curve in Fig. 5 (a).

Several mechanisms can contribute to ETB, depending on the applied field, ambient humidity and the particular layer-stack being tested. At low humidity levels, the accumulation of oxygen vacancies near the cathode region, electron injection and Schottky-barrier lowering are well-known mechanisms causing resistance degradation[47], [48], [56]–[58]. Under moderately temperature- and field-accelerated conditions, electrical degradation and dielectric breakdown typically occurs over a period of 10^5 - $10^6\ \text{s}$.

In high humidity, the presence of surface water can catalyze metal electromigration of e.g. Ag, Cu or Pd from the anode to the cathode side, creating dendrites which upon reaching a critical thickness induce ETB[30], [59]. The reduction of PbO and the formation of Pb-filaments is another proposed mechanism, but has, to the author's knowledge, not yet been observed. For the RH and layer-stack used in the current study, the data is consistent with water-electrolysis: $H_2O(g) \rightarrow H_2(g) + O_2(g)$ and electrochemical compression of gas trapped at the piezoelectric thin film/electrode interfaces, as the driving mechanism[24]. Degradation occurs through cracking, delamination, and failure by ETB, caused by dielectric breakdown through humidified air separating the top and bottom electrodes in cracks and voids. The more frequent

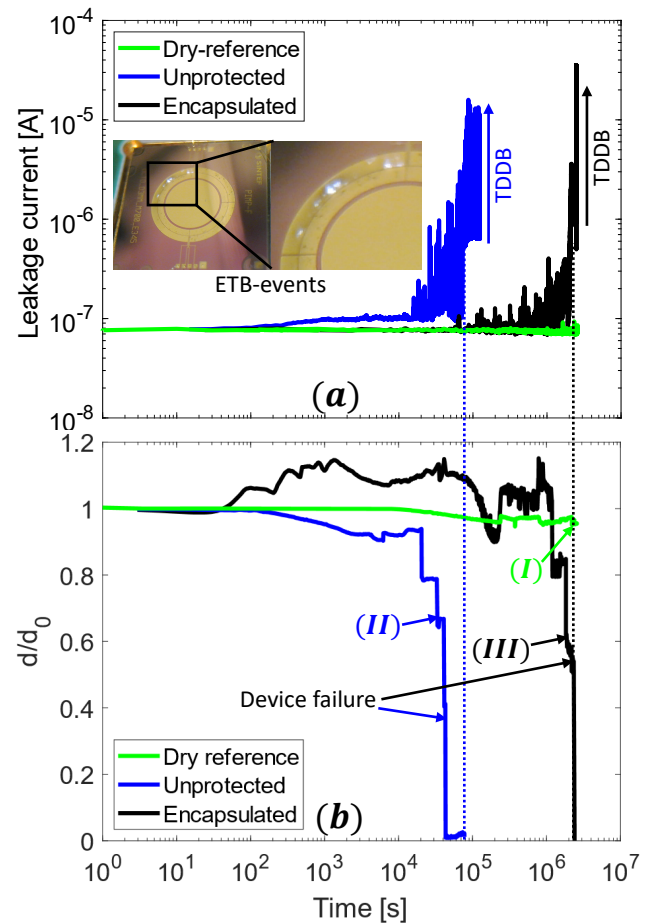


Fig. 5: Degradation during operation at 1.5 kHz as a function of time for bare (blue) and encapsulated (black) devices in humid conditions. The green curve shows a typical bare device in dry ambient for comparison. The measured leakage is shown in (a) and the relative angular deflection in (b). The inset shows recorded in-situ ETB-events, manifested as arcing, after about $3 \times 10^4\ \text{s}$. Pre-failure degradation was generally associated with leakage-transients and ETB, severely affecting both the angular deflection amplitude and the dynamic response of both bare and encapsulated devices, as illustrated by point (I)-(III) and further illustrated in Fig. 6.

appearance of ETB along the edges of the active positrodes is consistent with this model, as the edges offer easier pathways for transport and trapping of water molecules underneath the electrode. It is also suggested that a higher electrochemical activity of oxidized TiW adhesion-layer, could locally increase the evolution of $H^+/O_2(g)$ [60]–[66].

After the onset, the number of leakage transients increased with operation time, on both bare and encapsulated devices, consistent with the literature[67]. In contrast to degradation in dry conditions, transients in humid conditions arise from an interplay between cracking and water adsorption/desorption dynamics creating temporary current transients along the surface and in cracks/voids, as the device start to behave as an electrochemical cell[68]–[70]. This is supported by in-situ recordings of arcing-events showing a direct connection between transients and ETB, as exemplified for bare devices after about $3 \times 10^4\ \text{s}$ of degradation in the inset images of Fig. 5 (a). Hence for humid conditions, the onset of transients indicates the start of hard degradation-events and the beginning of device failure. Thus, only partial recovery of thin-film devices can be achieved by post-degradation annealing[28],

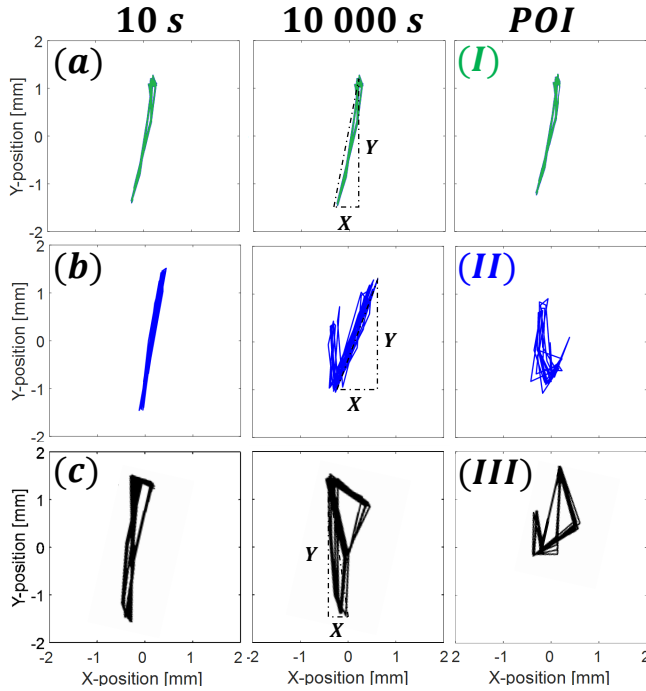


Fig. 6: X/Y laser-traces at the indicated points of Fig. 5 (b). Recorded responses of bare devices (blue) are shown in (a), encapsulated devices (black) in (b) in humid conditions. The dry reference is shown in (c). The X/Y positions indicate the points used for calculating the angular deflection-amplitude.

[71], and reduced reliability should be expected upon continued usage in humid as well as in dry conditions.

Angular deflection amplitude relative to the device's initial deflection amplitude is plotted as a function of degradation time for bare (blue curve) and encapsulated (black curve) devices in humid conditions in Fig. 5 (b). A measurement of a bare device in dry conditions (green curve) is included for reference. (I), (II) and (III) relates to points for which the laser-trace has been extracted, and are shown in Fig. 6.

In dry conditions, the degradation rate followed a logarithmic decay, characteristic for polarization- and electrical field-driven fatigue processes[72]–[75]:

$$d = d_0 - R \log(t) \quad (3)$$

Here d_0 is the initial deflection amplitude, R the rate of change, and t the time of operation in seconds. The decrease in angular deflection was $R_{\text{bare}} = 0.03 \pm 0.01$ %/decade and $R_{\text{encap}} = 0.11 \pm 0.05$ %/dec for the bare and encapsulated devices, respectively. Apart from the small amplitude loss with time, presumably from general fatigue-related mechanisms[75], devices operated with a stable linear response and no device failure for more than 5×10^9 unipolar cycles at 1.5 kHz (38 days) in dry conditions.

In humid conditions, the dynamic behavior of both bare and encapsulated devices differed significantly from the behavior at dry conditions after 10^2 s. Routing-breaches were manifested as four sudden losses (drops) of angular deflection, corresponding to each of the four actuated electrodes, as shown in Fig. 5 (b). The electrode losing electrical connection could be inferred by the change of the direction of the tip-tilt motion

from the X-Y laser traces. The probability of a certain routing to fail first, however, could not be identified. Complete device failure occurred at the last routing-breach, as indicated by the arrows in Fig. 5 (b).

ETB, cracking, and electrode delamination significantly altered the dynamic device behavior before failure. The recorded X- and Y-traces of the bare (blue) and encapsulated (black) devices, as shown in Fig. 6 (b)-(c), illustrates changes in the tip-tilt motion encountered during degradation. (I)-(III) shows the X/Y laser-traces after 10 s, 10 000 s, and at points of interest from Fig. 5 (b).

In general, distortions in the dynamic behavior were manifested as additional tilt-twist motions gradually deviating from the linear tilt-motion otherwise maintained in dry conditions. The SEM-analysis in Fig. 3 (b), indicates that these distortions are due to cracking, partial loss of electrode area, delamination, and de-clamping, changing the amplitude and distributed forces acting on the membrane. As exemplified in the 10 s column of Fig. 6 (a)-(c), minor distortions could be detected in a number of the pristine devices. Since thin-film devices work by the film-substrate stress-transfer making them sensitive to the in-plane circumstances of the film, minor distortions are assumed to stem from processing-related variations across the wafer. This includes small variations in film thickness, roughness, texture, or substrate clamping, and becoming more prominent for geometries using relatively large electrodes (here ~ 1.2 mm²). It is notable that, in humid conditions, the characteristic minor distortion of the tip-tilt motion evolve to become a major distortion as the degradation proceeds. This contrasts to operation in dry conditions, where the magnitude of the distortion remains throughout the experimental time, as is particularly clear when comparing the X/Y-scans after 10 and 10 000 s operation in Fig. 6 (a) and (c). This supports the findings and humidity related degradation mechanisms discussed above, i.e. the initiation and spreading of the degradation is related to the processing parameters. With time, degradation translates into relatively large macroscopic changes in the electromechanical response, including geometric nonlinearities, transverse resonance-shifts, angular deflection hysteresis, nonlinear normal modes, and bifurcations, prior to TDDB or loss of considerable deflection amplitude[76]–[80].

It is also plausible that the in-plane piezoelectric stresses combined with the residual stress generate cracks that initiate degradation, as both can be up to tens to hundreds of MPa[81]–[84]. As shown in the supplementary information, the PZT films used in this study were, based on wafer-bending measurements[85], [86], found to hold a tensile residual stress

Tab. 1: Characteristic values for the bare and encapsulated devices. The 95 % confidence intervals are given in the brackets for the MTTF-measurements.

Parameter	Bare	Encapsulated
A_{ETB} [μm^2] 95 % RH	150 ± 40	$27\,000 \pm 15\,000$
P_{200} kv/cm, init. [$\mu\text{C}/\text{cm}^2$]	30.6 ± 1.0	27.3 ± 0.4
$\epsilon_0 v$, initial	1218 ± 53	1066 ± 61
d_{200} kv/cm, initial [$^\circ$]	0.48 ± 0.06	0.45 ± 0.08
R_d 0% RH [%/dec]	-0.03 ± 0.01	-0.11 ± 0.05
R_{P_m} 0% RH [%/dec]	-0.45 ± 0.17	-0.67 ± 0.14
R_ϵ 0% RH [%/dec]	-0.001 ± 0.001	-0.010 ± 0.005
MTTF [s] 95 % RH	$\alpha = 2.7 \times 10^4 / \kappa = 1.5$ [$1.9 \times 10^4 - 4.0 \times 10^4$]	$\alpha = 1.1 \times 10^6 / \kappa = 2.0$ [$0.9 \times 10^6 - 1.5 \times 10^6$]
Failure type	Deflection loss	Deflection loss/TDDB

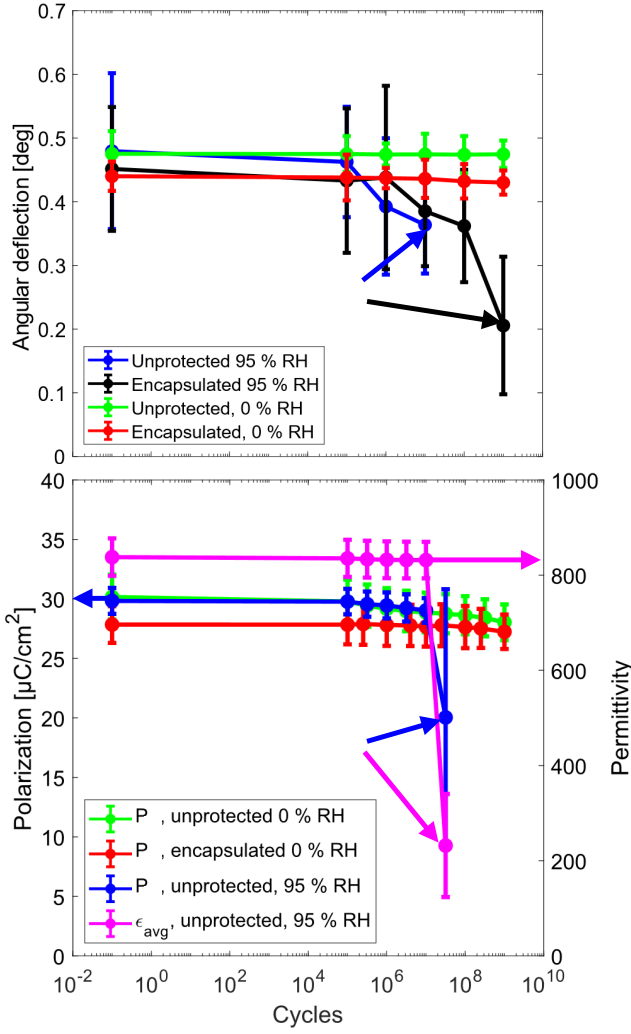


Fig. 7: Average angular deflection as a function of actuation cycles for bare dry (green), encapsulated dry (red), bare humid (blue) and encapsulated humid are shown in (a). The corresponding average polarization and permittivity (magenta) are shown in (b). The latter is here only plotted for unprotected devices in humid conditions for illustration purposes, noting that similar behavior was measured for encapsulated devices. P-E and ϵ -E loops can be found in the supplementary information. Pristine encapsulated devices showed a 6, 10 and 12 % decreased initial deflection amplitude, polarization and permittivity compared to pristine bare devices. Degradation characteristic for dry conditions was generally consistent with a polarization-fatigue model. In humid conditions, degradation manifested itself as a rapid loss of angular deflection after the onset of ETB. The onset of ETB started after $6.1 \pm 4.2 \times 10^3$ s ($9.2 \pm 6.3 \times 10^6$ cycles) for bare devices and after $7.9 \pm 3.8 \times 10^4$ s ($1.2 \pm 0.6 \times 10^8$ cycles) for encapsulated devices. The large sample-to-sample variations are reflected in the large standard deviations. Device failure due to the breach of an electrode routing is indicated by a drop in polarization, permittivity, and angular deflection.

of $\sigma_R = 270 \pm 70$ MPa post processing. At 200 kV/cm, the in-plane piezoelectric stress, $\sigma_X = -e_{31,f} E_Z(t)$ is 240 MPa tensile, which when added to the residual stress, surpass the critical stress of PZT (~ 550 MPa) during operation[74], [87]–[89]. Improved reliability in humid conditions may thus be achieved by reducing the number of post-processing defects, reduce the residual film stress, improve the mechanical limits of the piezoelectric and improve the mechanical strength of the humidity barrier[90].

C. Ferroelectric, dielectric, and piezoelectric properties

For ferroelectric and dielectric measurements, the cycling of selected samples was interrupted three times per decade to measure polarization-field (P-E) and permittivity-field (ϵ -E) hysteresis loops in both dry and humid conditions. The average maximum polarization (P_M), average permittivity (ϵ_{avg}), and angular deflection (d_{avg}) of five micromirrors as a function of degradation-cycles in dry and humid conditions are shown for bare and encapsulated devices in Fig. 7 (a) and (b). Device failure in humid operating conditions are indicated by the colored arrows. Typical P-E, ϵ -E and d-E hysteresis-loops can be found in the supplementary information, and corresponding average values are summarized in Tab. 1. For the pristine bare devices $d_{\text{max}} = 0.48 \pm 0.06^\circ$, $P_M = 30.6 \pm 1.0 \mu\text{C}/\text{cm}^2$, $E_C^+ = 27.5 \pm 1.2$ kV/cm, $\epsilon_{0V} = 1218 \pm 53$ and $\tan(\delta) = 0.08 \pm 0.03$ respectively. For the encapsulated devices, $d_{\text{max}} = 0.45 \pm 0.08^\circ$, $P_{\text{max}} = 27.3 \pm 0.4 \mu\text{C}/\text{cm}^2$, $\epsilon_{0V} = 1066 \pm 61$. This corresponds to a reduction of about 6, 11 and 12 % compared to the properties of the bare devices. Since $e_{31,f} \propto P\epsilon$, reduced piezoelectric properties fits well with a reduction in P and ϵ and are ascribed to the additional encapsulation-step. Though not distinguished here, factors such the incorporation of protonic defects into PZT during the ALD process[91]–[93] or thermal hysteresis[94], may impede the piezoelectric response and decrease the overall reliability of piezoMEMS-devices. Furthermore, while degradation-rates of $R_{P_M} = -0.45$ %/dec, $R_{\epsilon_{\text{avg}}} = -0.009$ %/dec and $R_d = -0.03$ %/dec for the bare devices, the degradation-rates increased to $R_{P_M} = -0.67$ %/dec, $R_{\epsilon_{\text{avg}}} = -0.010$ %/dec and $R_d = -0.11$ %/dec for the encapsulated devices. These results correlate well with a polarization-fatigue driven model; the extrinsic contributions to the piezoelectric effect degrades by the immobilization of domain walls in which the accumulation of oxygen vacancies near the cathode region is particularly important[43], [44], [95], [96]. Although the devices will degrade electrically with time, eventually resulting in TDDB also in dry conditions, the present results indicate that the devices should be able to survive for realistic product lifetimes without failure [97]–[100].

In humid conditions, the measured decrease in ferroelectric, dielectric and piezoelectric properties with time were found to be directly connected to the loss of electrode area due to ETB, as detailed in the supplementary information. When corrected for, no significant degradation exceeding the dry-equivalent behavior was measured. This correlates well with degradation driven by water electrolysis, which is a DC phenomenon, and will "freeze-out" in the high AC frequency range (>100 Hz).

IV. CONCLUSIONS

In conclusion, the median time-to-failure of thin-film PZT-based micromirrors increased from 2.7×10^4 s [$1.9 \times 10^4 - 4.0 \times 10^4$ s] to 1.1×10^6 s [$0.9 \times 10^6 - 1.5 \times 10^6$ s] under continuous unipolar operation at 0-200 kV/cm in 95 % relative humidity, by using a 40 nm thick Al_2O_3 barrier-layer deposited by atomic-layer deposition. Using a relatively small number of samples, important effects of humidity-related degradation on reliability and dynamic behavior of factual piezoMEMS devices have been highlighted. In the pristine state,

encapsulated devices displayed a reduction in maximum polarization, permittivity, and initial angular deflection of about 10, 12 and 6 %, relative to bare devices, accompanied with an increased fatigue-rate compared to the bare devices in dry conditions. The reduction in initial performance was ascribed to effects related to the additional encapsulation step. Consistent with degradation-mechanisms proposed in previous work, the early onset of leakage-transients connected with electrothermal breakdown-events, preceded that of time-dependent dielectric breakdown, and was found to drive device degradation in humid conditions. Post-failure craters appeared more frequently along the edges of the biased top electrode, and at sites containing processing-related defects, including cracks, sputtered particles or nonuniform coverage of the humidity barrier-layer. For the bare devices, the size of each post-failure crater was $150 \pm 40 \mu\text{m}^2$ while for the encapsulated devices, the size of each crater was $27\,000 \pm 15\,000 \mu\text{m}^2$. This was consistent with degradation initiated at and spreading from a singularity of various processing-related defects. For all bare devices, the electrode routings were identified as the critical flaw, resulting in failure by the loss of angular deflection. For the encapsulated devices, on the other hand, failure was due to a combination of time-dependent dielectric breakdown, routing breaches and loss of angular deflection.

In all cases of degradation, cracks, delaminated electrodes, and craters from electrothermal breakdown-events severely affected the devices' dynamic behavior, and reduced device-reliability before failure. Signature tip-tilt motions, presumably originating from minor processing related defects, intensified as degradation preceded in humid conditions, as opposed to remaining stable in dry conditions. Apart from the loss of electrode area, however, humidity induced degradation was not found to significantly alter the polarizability, permittivity, or loss.

V. REFERENCES

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